

M3D111

# **2N3019**

## **NPN medium power transistor**

Product specification

1997 Jun 19

Supersedes data of September 1994

File under Discrete Semiconductors, SC04

**NPN medium power transistor****2N3019****FEATURES**

- High current (max. 1 A)
- Low voltage (max. 80 V).

**APPLICATIONS**

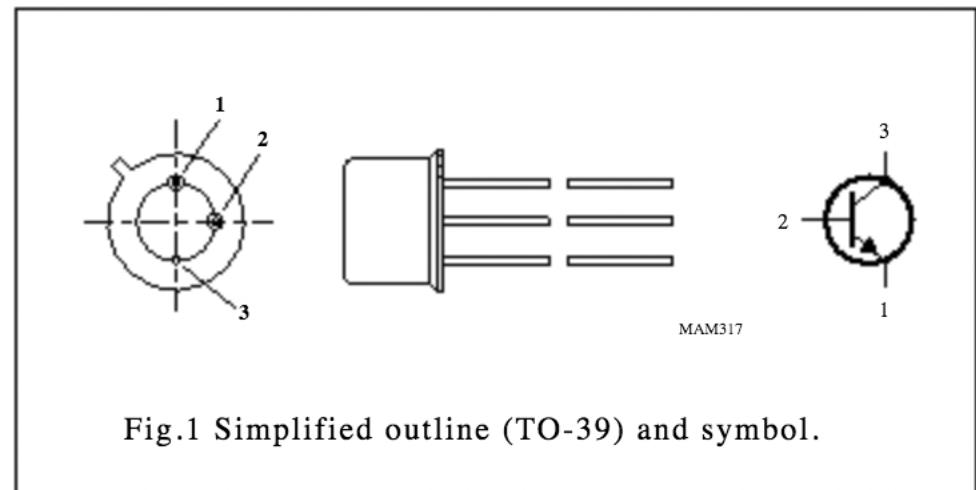
- Amplifier and switching circuits.

**DESCRIPTION**

NPN medium power transistor in a TO-39 metal package.

**PINNING**

PIN	DESCRIPTION
1	emitter
2	base
3	collector, connected to case

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	140	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	80	V
I <sub>c</sub>	collector current (DC)		-	1A	
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	-	800	mW
		T <sub>case</sub> ≤ 25 °C	-	5W	
h <sub>FE</sub>	DC current gain	I <sub>c</sub> = 150 mA; V <sub>CE</sub> = 10 V	100	300	
f <sub>T</sub>	transition frequency	I <sub>c</sub> = 50 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	100	-	MHz

## NPN medium power transistor

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**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	—	140	V
V <sub>CEO</sub>	collector-emitter voltage	open base	—	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	—	7V	
I <sub>c</sub>	collector current (DC)		—	1A	
I <sub>CM</sub>	peak collector current		—	1A	
I <sub>BM</sub>	peak base current		—	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	—	800	mW
		T <sub>case</sub> ≤ 25 °C	—	5W	
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		—	200	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>thj-a</sub>	thermal resistance from junction to ambient	in free air	218	K/W
R <sub>thj-c</sub>	thermal resistance from junction to case		35	K/W

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## CHARACTERISTICS

 $T_{amb}=25\text{ }^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_{CBO}$	collector cut-off current	$I_E = 0; V_{CB} = 90\text{V}$	-	10	nA
		$I_E = 0; V_{CB} = 90\text{V}; T_{amb} = 150\text{ }^{\circ}\text{C}$	-	10	$\mu\text{A}$
$I_{EBO}$	emitter cut-off current	$I_C = 0; V_{EB} = 5\text{V}$	-	10	nA
$h_{FE}$	DC current gain	$V_{CE} = 10\text{ V}; \text{note 1}$			
		$I_C = 0.1\text{ mA}$	50	-	
		$I_C = 10\text{mA}$	90	-	
		$I_C = 150\text{ mA}$	100	300	
		$I_C = 150\text{ mA}; T_{case} = -55\text{ }^{\circ}\text{C}40$		-	
		$I_C = 500\text{ mA}$	50	-	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 150\text{ mA}; I_B = 15\text{mA}$	-	200	mV
		$I_C = 500\text{ mA}; I_B = 50\text{ mA}; \text{note 1}$	-	500	mV
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 150\text{ mA}; I_B = 15\text{ mA}; \text{note 1}$	-	1.1	V
$C_c$	collector capacitance	$I_E = i_e = 0; V_{CB} = 10\text{ V}; f = 1\text{ MHz}$	-	12	pF
$C_e$	emitter capacitance	$I_C = i_c = 0; V_{EB} = 500\text{ mV}; f = 1\text{ MHz}$	-	60	pF
$f_T$	transition frequency	$I_C = 50\text{ mA}; V_{CE} = 10\text{ V}; f = 20\text{ MHz}$	100	-	MHz
F	noise figure	$I_C = 0.1\text{ mA}; V_{CE} = 5\text{V}; R_s = 1\text{k}\Omega;$ $f = 1\text{ kHz}; B = 200\text{ Hz}$	-	4 d B	

## Note

1. Pulse test:  $t_p \leq 300\text{ }\mu\text{s}; \delta \leq 0.01$ .

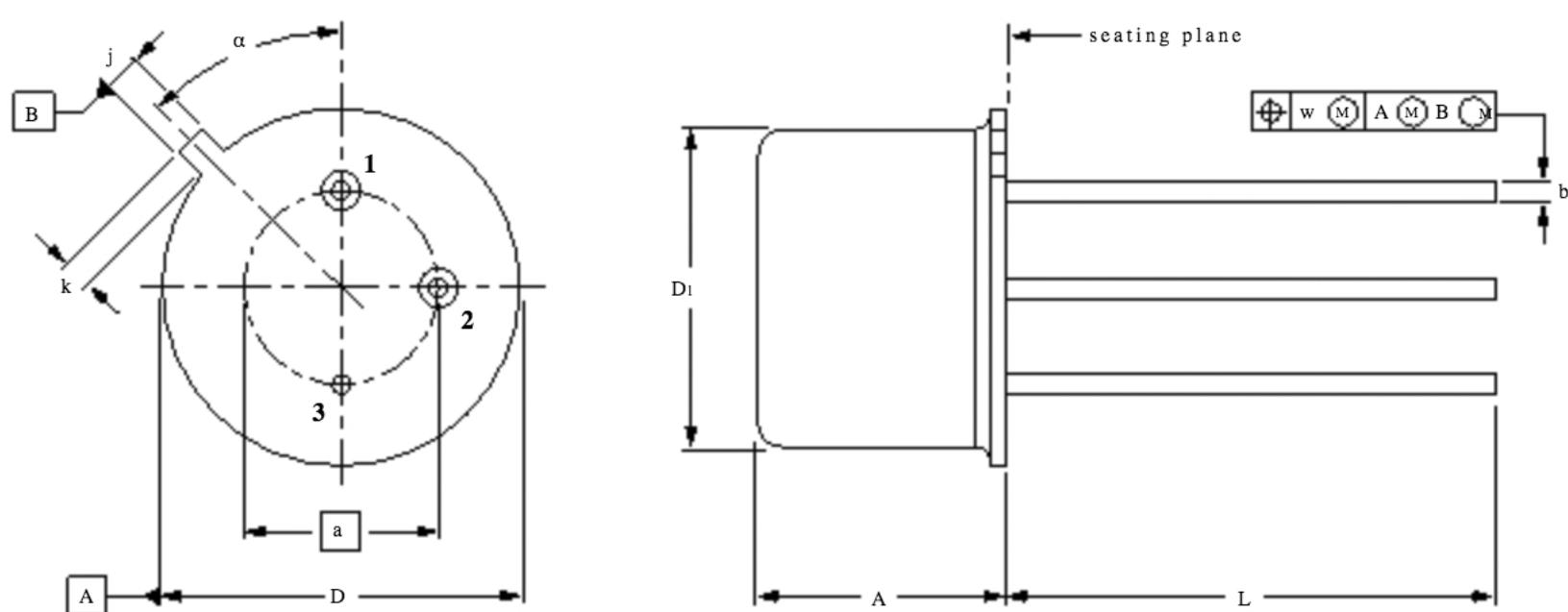
## NPN medium power transistor

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## PACKAGE OUTLINE

Metal-can cylindrical single-ended package; 3 leads

SOT5/11



DIMENSIONS (mm are the original dimensions)

UNIT	A	a	b	D	D <sub>1</sub>	j <sub>k</sub>	w	α
mm	6.60 6.35	5.08 4.41	0.48 0.41	9.39 9.08	8.33 8.18	0.85 0.75	0.95 0.75	45°

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT5/11		TO-39				97-04-11

**NPN medium power transistor****2N3019****DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

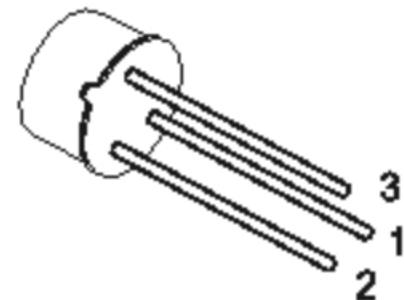
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## HIGH CURRENT, HIGH FREQUENCY AMPLIFIERS

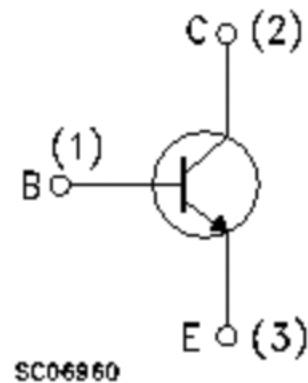
### DESCRIPTION

The 2N3019 is a silicon planar epitaxial NPN transistors in Jedec TO-39 metal case, designed for high-current, high frequency amplifier application. It feature high gain and low saturation voltage.



TO-39

### INTERNAL SCHEMATIC DIAGRAM



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V <sub>CBO</sub>	Collector-Base Voltage (I <sub>E</sub> =0)	140	V	
V <sub>CEO</sub>	Collector-Emitter Voltage (I <sub>B</sub> =0)	80	V	
V <sub>EBO</sub>	Emitter-Base Voltage (I <sub>C</sub> =0)	7	V	
I <sub>C</sub>	Collector Current	1	A	
P <sub>tot</sub>	Total Dissipation at Tamb at T case	≤ 25 °C ≤ 25 °C	0.8 5	W W
T <sub>stg</sub>	Storage Temperature	-65 to 200	°C	
T <sub>j</sub>	Max. Operating Junction Temperature	200	°C	

## THERMAL DATA

R <sub>thj-case</sub> R <sub>thj-amb</sub>	Thermal Resistance Junction-Case Thermal Resistance Junction-Ambient	Max Max	35 219	°C/W °C/W
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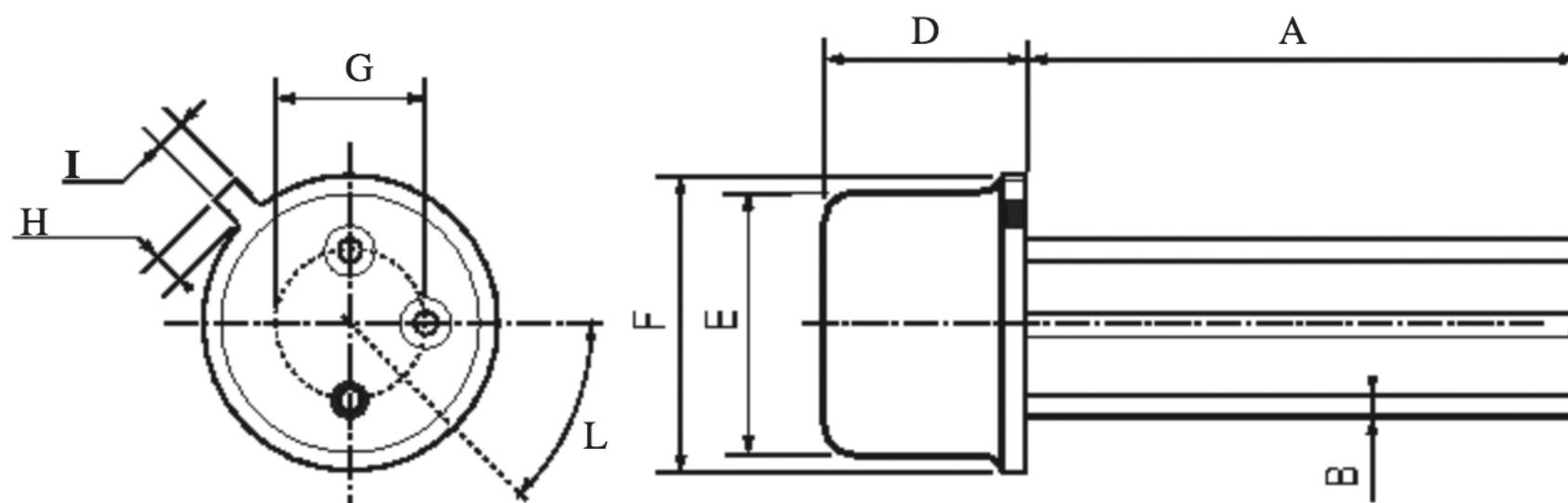
ELECTRICAL CHARACTERISTICS (T<sub>case</sub> = 25 °C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I <sub>CBO</sub>	Collector Cut-off Current (I <sub>E</sub> =0)	V <sub>CB</sub> =90 V V <sub>CB</sub> =90 V T <sub>case</sub> =150 °C			10 10	nA μA
I <sub>EBO</sub>	Emitter Cut-off Current (I <sub>c</sub> =0)	V <sub>EB</sub> =5 V			10	nA
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage (I <sub>E</sub> =0)	I <sub>c</sub> =100 μA	140			V
V <sub>(BR)CEO</sub>	* Collector-Emitter Breakdown Voltage (I <sub>B</sub> =0)	I <sub>c</sub> =10 mA	80			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage (I <sub>c</sub> =0)	I <sub>E</sub> =100 μA 7				V
V <sub>CE(sat)</sub>	* Collector-Emitter Saturation Voltage	I <sub>c</sub> =150 mA I <sub>B</sub> =15 mA I <sub>c</sub> =500 mA I <sub>B</sub> =50 mA			0.2 0.5	V V
V <sub>BE(sat)</sub>	* Base-Emitter Saturation Voltage	I <sub>c</sub> =150 mA I <sub>B</sub> =15 mA			1.1	V
h <sub>FE</sub> *	DC Current Gain	I <sub>c</sub> =0.1 mA V <sub>CE</sub> =10 V I <sub>c</sub> =10 mA V <sub>CE</sub> =10 V I <sub>c</sub> =150 mA V <sub>CE</sub> =10 V I <sub>c</sub> =500 mA V <sub>CE</sub> =10 V I <sub>c</sub> =1A V <sub>CE</sub> =10 V I <sub>c</sub> =150 mA V <sub>CE</sub> =10 V T <sub>amb</sub> =-55 °C	50 90 100 50 15 40		300	
h <sub>fe</sub> *	Small Signal Current Gain	I <sub>c</sub> =1 mA V <sub>CE</sub> =5 V f = 1 KHz	80		400	
f <sub>T</sub>	Transition Frequency	I <sub>c</sub> =50 mA V <sub>CE</sub> =10 V f=20MHz	100			MHz
C <sub>CBO</sub>	Collector Base Capacitance	I <sub>E</sub> =0 V <sub>CB</sub> =10 V f = 1 MHz			12	pF
C <sub>EBO</sub>	Emitter Base Capacitance	I <sub>c</sub> =0 V <sub>EB</sub> =0.5 V f = 1 MHz			60	pF
NF	Noise Figure	I <sub>c</sub> =0.1 mA V <sub>CE</sub> =10 V f = 1 KHz R <sub>g</sub> =1K Ω			4 d	B
r <sub>bb'Cb'c</sub>	Feedback Time Constant	I <sub>c</sub> =10 mA V <sub>CE</sub> =10 V f = 4 MHz			400	ps

\* Pulsed: Pulse duration = 300 μs, duty cycle ≤ 1 %

## TO-39 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	12.7			0.500		
B	0 . 4 9					0.019
D	6 . 6					0.260
E	8 . 5					0.334
F	9 . 4					0.370
G	5.08			0.200		
H	1 . 2					0.047
I	0 . 9					0.035
L	4	5		0 (typ.)		



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